



Sheet 1 of 1

 <b>U.S. DEPARTMENT OF COMMERCE</b>  <b>PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)	<b>ATTY. DOCKET NO.</b>	<b>SERIAL NO.</b>
	<i>H1938</i>	10/601,401
	<b>APPLICANT</b>	
	Zoran Krivokapic	
<b>FILING DATE</b>	<b>GROUP</b>	
June 23, 2003	2812	

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
<i>SDI</i>	2004/044992	05/27/04	WO			

**OTHER DOCUMENTS (Including Author, Title, Date Pertinent Pages, Etc.)**

~~SDT~~ K. Ota, K. Sugihara, H. Sayama, T. Uchida, H. Oda, T. Eimori, H. Morimoto and Y. Inoue, Novel Locally Strained Channel Technique for High Performance 55nm CMOS, IEDM Technical Digest, December 8, 2002, pp. 27-30.

~~SDT~~ A. Shimizu, K. Hachimine, N. Ohki, H. Ohta, M. Koguchi, Y. Nonaka, H. Sato, and F. Ootsuka, Local Mechanical-Stress Control (LMC): A New Technique for CMOS-Performance Enhancement, IEDM Technical Digest, December 2, 2001, pp. 433-436.

**EXAMINER**

Stanette Sase

DATE 4/1  
CONSIDERED

**EXAMINER:** Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.